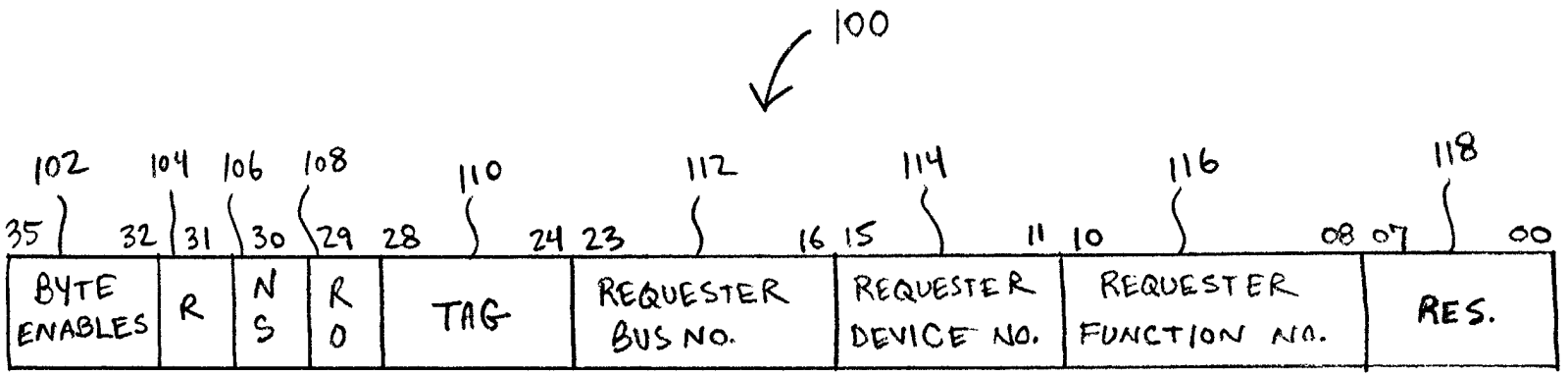

FIG. 1

AMPAD
22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

TOTE80 ZTSHTM60

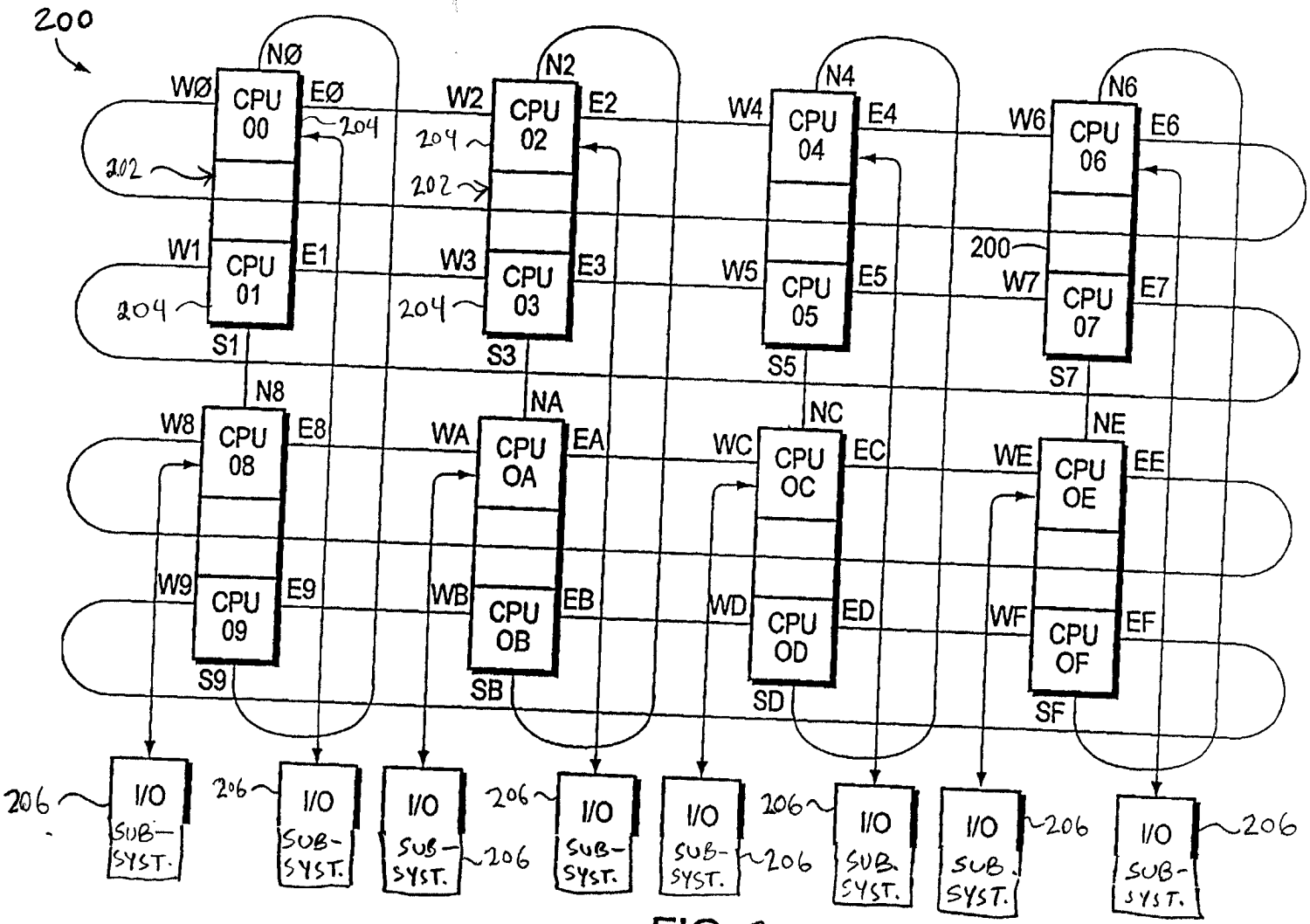

FIG. 2

TOTE80 "ZFSmth60

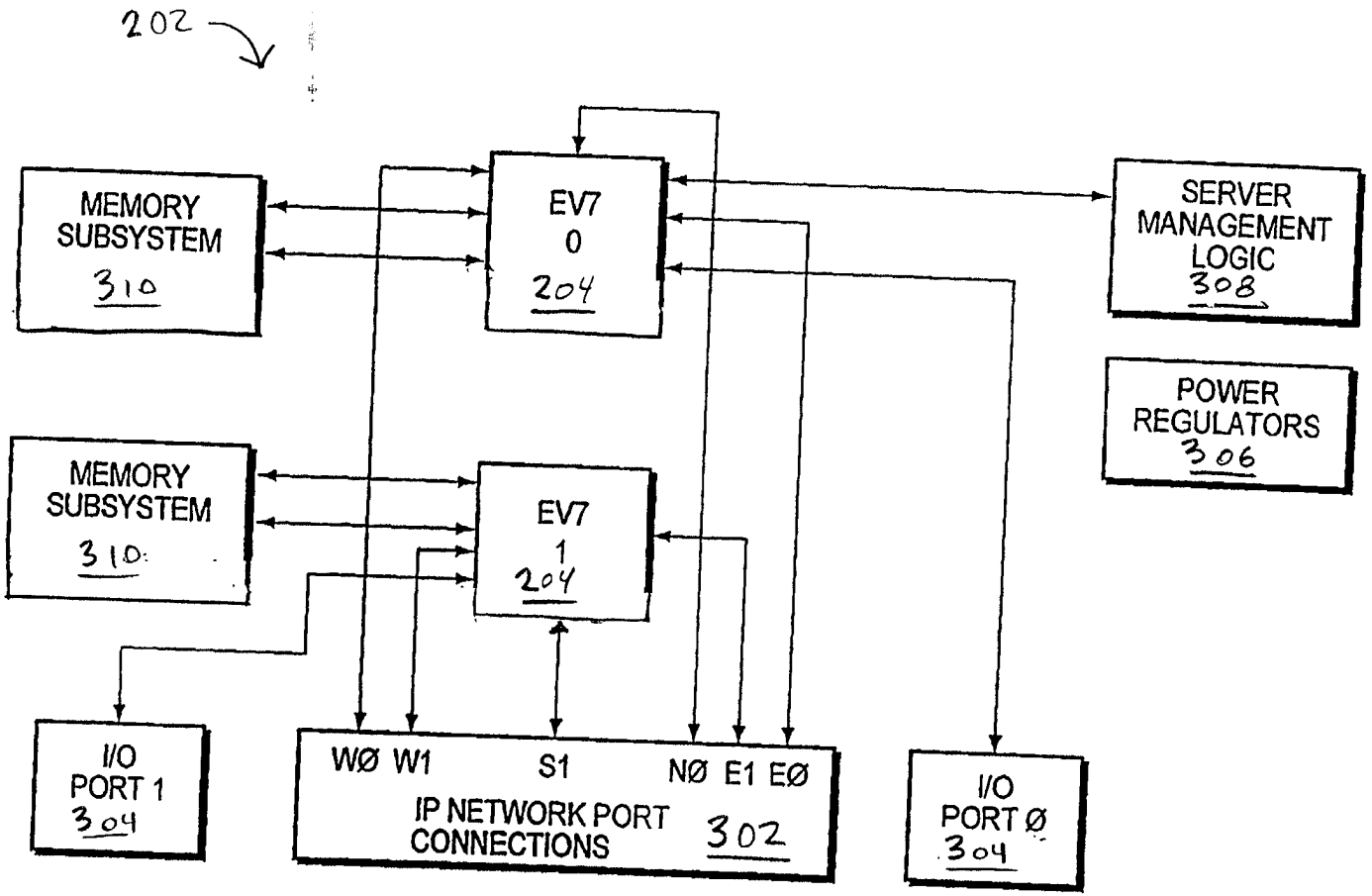

FIG. 3

TOTEBO 27 5mm66 AMPAD
22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

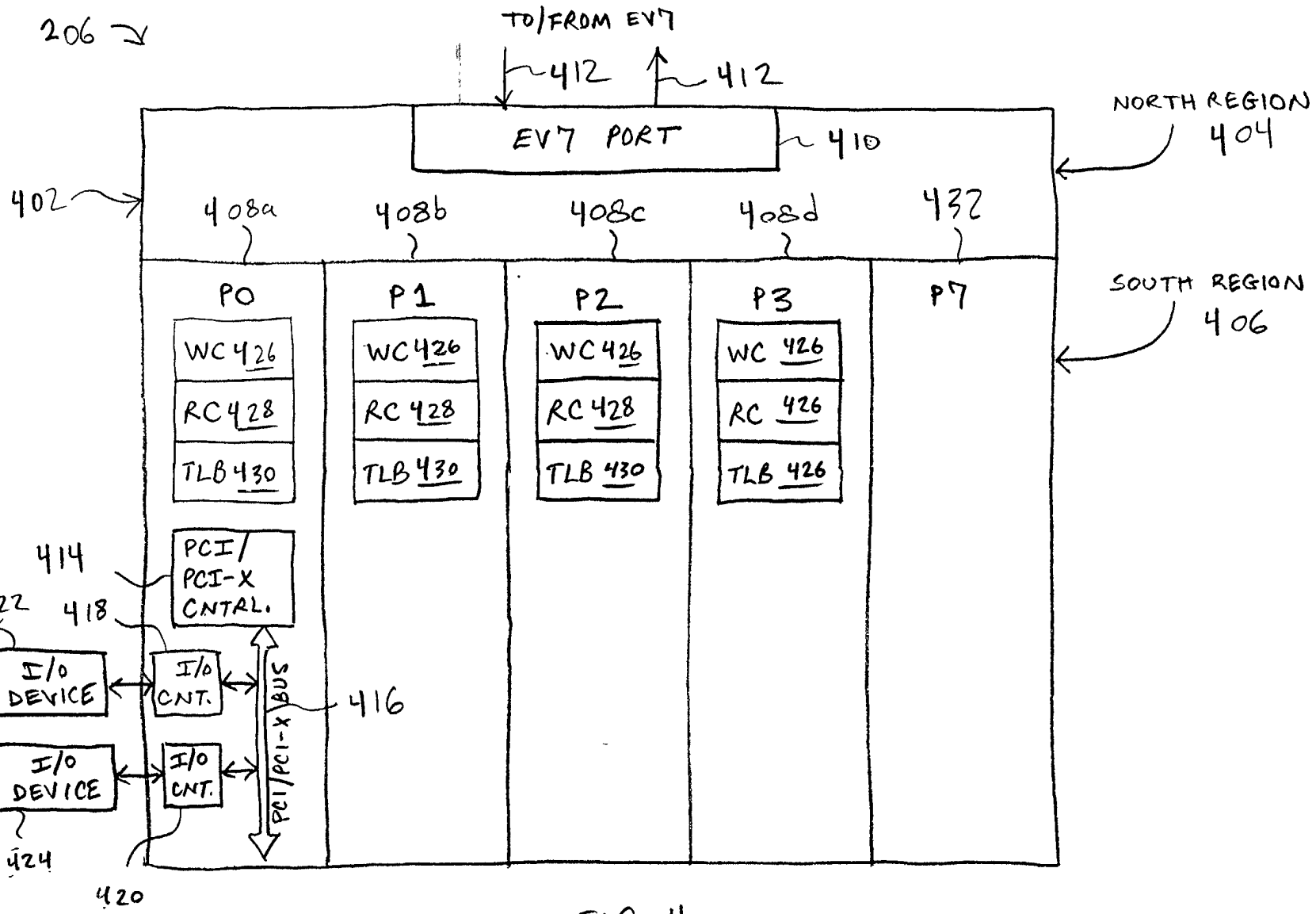

FIG. 4

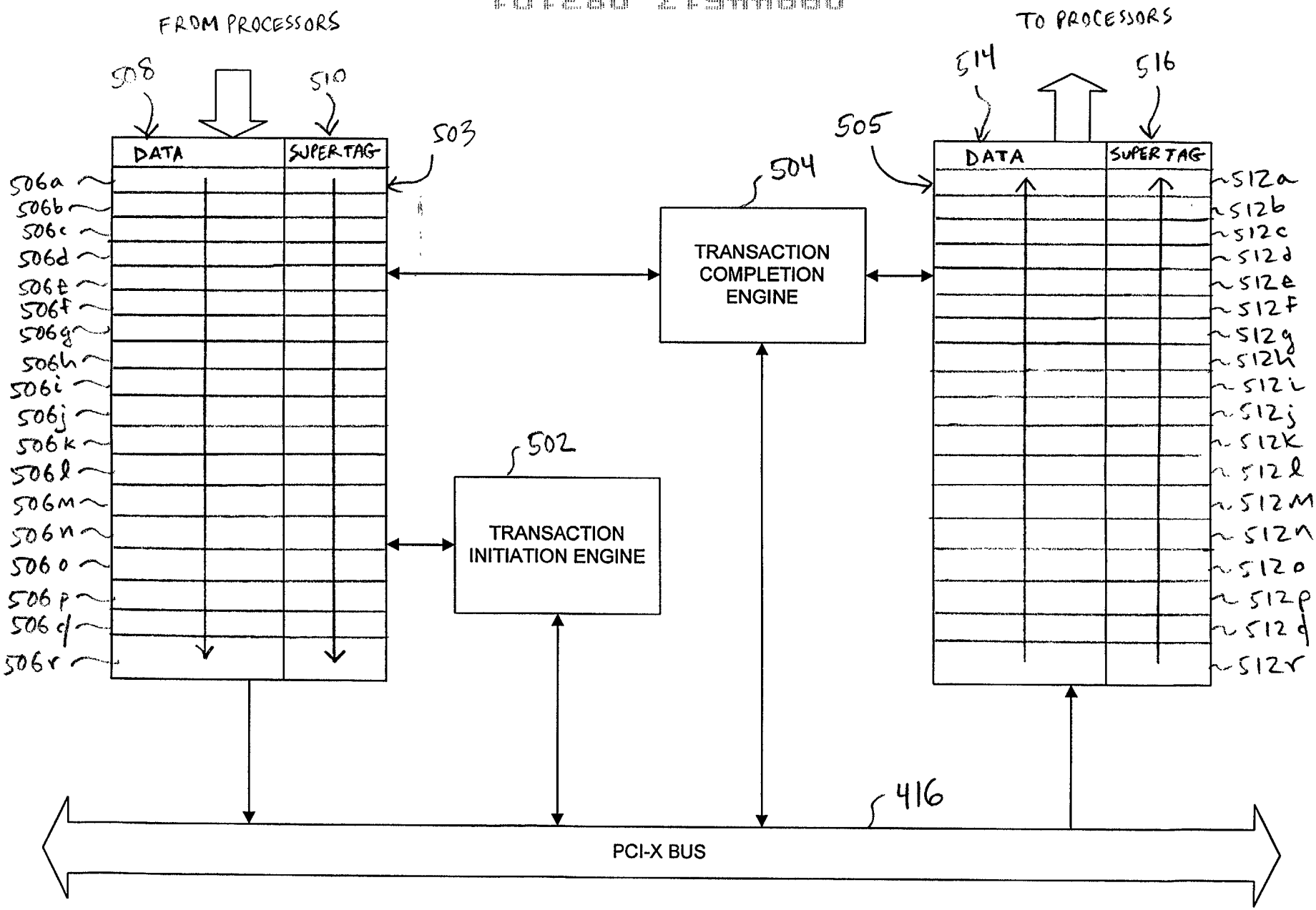
FROM PROCESSORS
TO PROCESSORS
508
510
514
516
503
505
504
502
DATA
SUPER TAG
TRANSACTION COMPLETION ENGINE
TRANSACTION INITIATION ENGINE
506a
506b
506c
506d
506e
506f
506g
506h
506i
506j
506k
506l
506m
506n
506o
506p
506q
506r
512a
512b
512c
512d
512e
512f
512g
512h
512i
512j
512k
512l
512m
512n
512o
512p
512q
512r
416
PCI-X BUS

FIG. 5

FOTEBO "ZFSmb6O

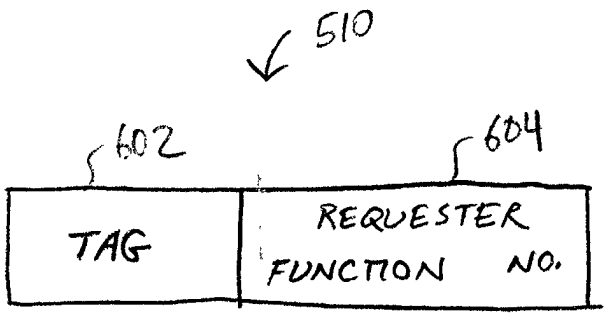

FIG. 6

AMPAD
22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

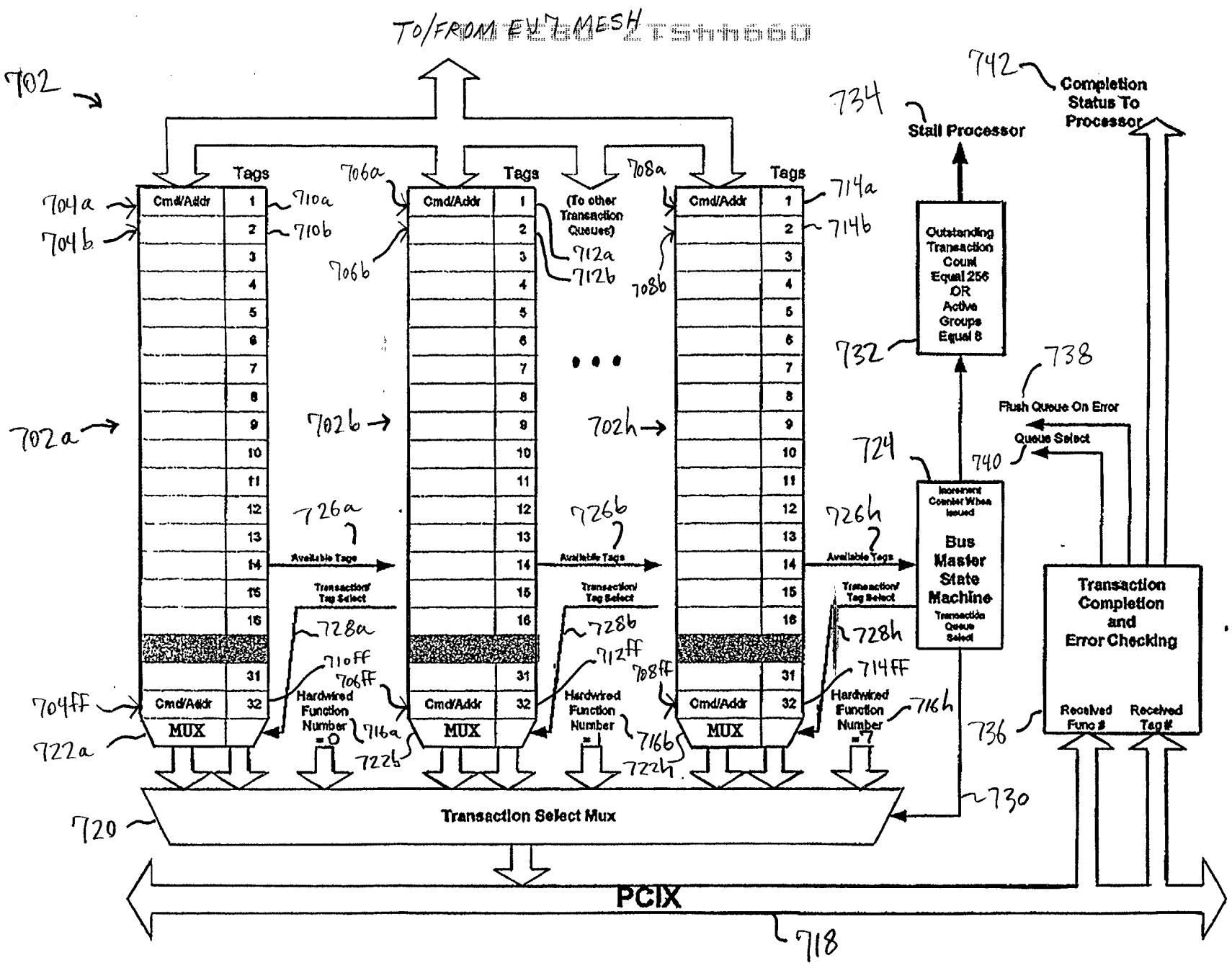
TO/FROM EV7 MESH
702
734
Stall Processor
742
Completion Status To Processor
Tags
Cmd/Addr
704a
704b
704ff
710a
710b
710ff
706a
706b
706ff
708a
708b
708ff
712a
712b
712ff
714a
714b
714ff
(To other Transaction Queues)
Outstanding Transaction Count Equal 256 OR Active Groups Equal 8
732
738
Flush Queue On Error
Queue Select
740
724
Increment Counter When Issued
Bus Master State Machine
Transaction Queue Select
726a
726b
726h
Available Tags
Transaction/ Tag Select
728a
728b
728h
Hardwired Function Number
=0
=7
716a
716b
716h
MUX
722a
722b
722h
Transaction Completion and Error Checking
Received Func #
Received Tag #
736
730
Transaction Select Mux
720
PCIX
718
702a
702b
702h

Fig. 7